

ABSTRACT OF THE DISCLOSURE

The output lines are connected to the signal lines. The gradation voltage lines are arranged to intersect the output lines. Two switch drive lines are provided for one gradation voltage line. The trigger lines are provided in parallel with the output lines. The switch circuits are provided near the intersections at which those wiring lines intersect each other. The decoder to which the multi-tone image data is fed produces data of "1" on one single switch drive line, and data of "0" on the other switch drive lines. The trigger pulses are sequentially supplied to the trigger lines. Data of "1" is stored in the switch circuit connected to the switch drive line of "1". The gradation voltages from the gradation voltage lines connected to the switch circuit in which data of "1" is stored are supplied via the output lines to the signal lines.

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